

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/024,178		12/21/2001	Jae Young Chung	2658-0277P	4389
2292	7590	04/21/2005		EXAMINER	
BIRCH S	TEWAR	T KOLASCH &	ERDEM, FAZLI		
PO BOX 7- FALLS CH		VA 22040-0747		ART UNIT	PAPER NUMBER
111220 01				2826	
			DATE MAILED, 04010005		

DATE MAILED: 04/21/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

 ;		A 11 41 A4					
		Application No.	Application No. Applicant(s)				
•,	Office Action Summers	10/024,178	CHUNG, JAE YO	CHUNG, JAE YOUNG			
	Office Action Summary	Examiner	Art Unit				
		Fazli Erdem	2826				
Period fo	The MAILING DATE of this communication or Reply	n appears on the cover sheet w	ith the correspondence ad	dress			
THE - Exte after - If the - If NC - Failt Any	ORTENED STATUTORY PERIOD FOR R MAILING DATE OF THIS COMMUNICATI nsions of time may be available under the provisions of 37 C SIX (6) MONTHS from the mailing date of this communication of period for reply specified above is less than thirty (30) days, of period for reply is specified above, the maximum statutory period for reply within the set or extended period for reply will, by reply received by the Office later than three months after the ed patent term adjustment. See 37 CFR 1.704(b).	ON. FR 1.136(a). In no event, however, may a in. a reply within the statutory minimum of thir veriod will apply and will expire SIX (6) MON statute, cause the application to become At	reply be timely filed ty (30) days will be considered timely ITHS from the mailing date of this co BANDONED (35 U.S.C. § 133).	<i>r.</i> ommunication.			
Status							
1)⊠	Responsive to communication(s) filed on	03 February 2005.					
2a)⊠	This action is FINAL . 2b)	This action is non-final.					
3)□	Since this application is in condition for all closed in accordance with the practice unc			merits is			
Disposit	ion of Claims						
4)⊠ 5)⊠ 6)⊠	Claim(s) <u>1-19</u> is/are pending in the applica 4a) Of the above claim(s) is/are with Claim(s) <u>12-19</u> is/are allowed.	hdrawn from consideration.	·				
Applicat	ion Papers						
9)□	The specification is objected to by the Exa	miner.					
	The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.						
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
	Replacement drawing sheet(s) including the α	prrection is required if the drawing	(s) is objected to. See 37 CF	R 1.121(d).			
11)	The oath or declaration is objected to by the	ne Examiner. Note the attached	d Office Action or form PT	O-152.			
Priority ι	ınder 35 U.S.C. § 119						
a)l	Acknowledgment is made of a claim for for All b) Some * c) None of: 1. Certified copies of the priority docur 2. Certified copies of the priority docur 3. Copies of the certified copies of the application from the International Business the attached detailed Office action for a	ments have been received. ments have been received in A priority documents have been ureau (PCT Rule 17.2(a)).	pplication No received in this National \$	Stage			
Attachmen	• •						
	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948	4) 🔲 Interview S Paper No(s	ummary (PTO-413) s)/Mail Date				
3) 🔲 Inforr	nation Disclosure Statement(s) (PTO-1449 or PTO/SI r No(s)/Mail Date		nformal Patent Application (PTO	-152)			

Application/Control Number: 10/024,178

DETAILED ACTION

Response to Arguments

Applicant's arguments filed 2/03/2005 have been fully considered but they are not persuasive. Lim et al. disclose a thin film transistor array substrate for liquid crystal display and a method for fabricating the same where in Fig. 9, two storage capacitors "M" and "N" are disposed between gate line 206 and capacitor electrode 216 formed above the gate line 206 where the gate line 206 is connected via contact holes 209 and 211 and connecting electrode 219 passing through storage capacitors "M" and "N" to the capacitor electrode 216.

Allowable Subject Matter

- 1. Claims 12-19 allowed.
- 2. Claims 2-11 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 4. Claim 1 rejected under 35 U.S.C. 102(e) as being anticipated by Lim et al. (6,448,579)

passing through storage capacitors "M" and "N" to the capacitor electrode 216

Lim et al. disclose a thin film transistor array substrate for liquid crystal display and a method for fabricating the same where in Fig. 9, two storage capacitors "M" and "N" are disposed between gate line 206 and capacitor electrode 216 formed above the gate line 206 where the gate line 206 is connected via contact holes 209 and 211 and connecting electrode 219

Page 3

The applied reference has a common assignee with the instant application. Based upon the earlier effective U.S. filing date of the reference, it constitutes prior art under 35 U.S.C. 102(e). This rejection under 35 U.S.C. 102(e) might be overcome either by a showing under 37 CFR 1.132 that any invention disclosed but not claimed in the reference was derived from the inventor of this application and is thus not the invention "by another," or by an appropriate showing under 37 CFR 1.131.

Conclusion

5. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Application/Control Number: 10/024,178

Art Unit: 2826

Page 4

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Fazli Erdem whose telephone number is (571) 272-1914. The

examiner can normally be reached on M - F 8:00 - 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Nathan Flynn can be reached on (571) 272-1915. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

FE

April 14, 2005

NATHAN J. FLYNN SUPERVISORY PATENT EXAMINÉR

TECHNOLOGY CENTER 2800